



NPN Triple Diffused Planar Silicon Transistor

Color TV Horizontal Deflection Output Applications(with Damper Diode)

©1755B

Applications

. High-voltage, power switching

Features

- . Fast speed (t_fmax=0.4us).
 . High reliability (Adoption of HVP process).
- . High breakdown voltage ($V_{\rm CBO}$ =1500V). . Micaless package facilitating mounting operation.

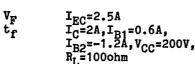
Absolute Maximum Ratings at Ta= Collector to Base Voltage Collector to Emitter Voltage Emitter to Base Voltage Collector Current	25°C V _{CBO} V _{CEO} V _{EBO} I _C		1500 800 6 2.5	unit V V V A
Peak Collector Current Collector Dissipation Junction Temperature Storage Temperature	1cp PC Tj Tstg	T _C =25 ^o C	10 50 150 -55 to +150	oC M

Electrical Characteristics at Ta=25°C Collector Cutoff Current Emi DC Gai Col Sat Bas Sat Col Bre Col Bre

agreement agreement agreement
Emitter Cutoff Current
DC Current Gain
Gain Bandwidth Product
Collector to Emitter
Saturation Voltage
Base to Emitter
Saturation Voltage
Collector to Base
Breakdown Voltage
Collector to Emitter
Breakdown Voltage
Emitter to Base
Breakdown Voltage
Diode Forward Voltage
Fall Time

ICBO	V _{CB} =800V,I _E =0
IEBO	V _{EB} =4V,I _C =0
hFE	V _{CE} =5V,I _C =0.5A
fT	V _{CE} =10V,I _C =0.5A
VCE(sat)	I _C =2A,I _B =0.6A
Vancous	I=2A.T=0.6A

VBE(sat)	$I_{C}=2A,I_{B}=0.6A$
V(BR)CBO	$I_{C}=5mA,I_{E}=0$
V(BR)CEO	I_{C} =100mA, R_{BE} = ∞
V(BR)EBO	I _E =200mA, I _C =0



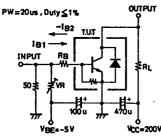
40 8		10 130	uA mA
	3	8	MHz V
		1,.5	V

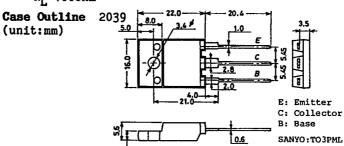
min typ max unit



2	V
0.4	us

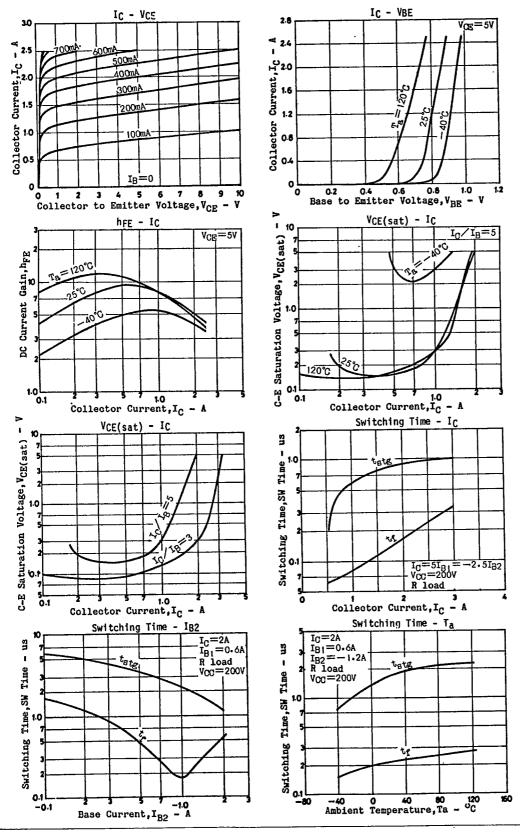
Switching Time Test Circuit





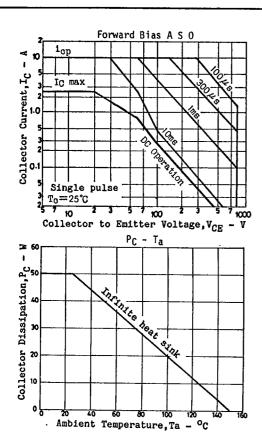
5277KI/D244KI,TS No.1755-1/3

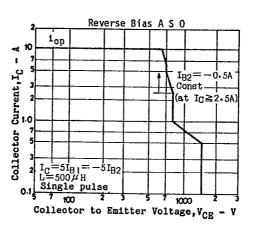
T-33-11 - _



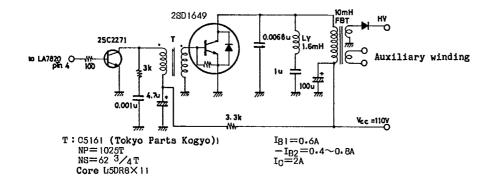
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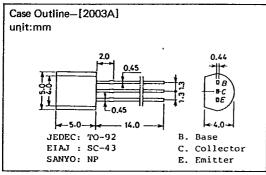


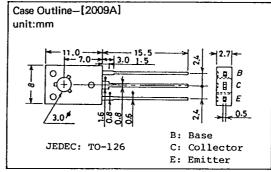
Sample Application Circuit

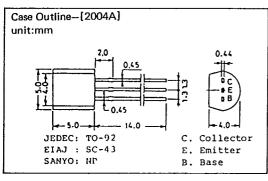


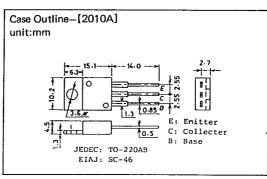
CASE OUTLINES AND ATTACHMENTS

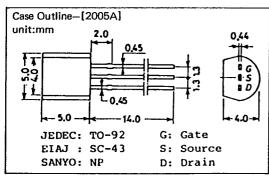
- All of Sanyo Transistor case outlines are illustrated below.
- •All dimensions are in mm, and dimensions which are not followed by min. or max. are represented by typical values.
- •No marking is indicated.

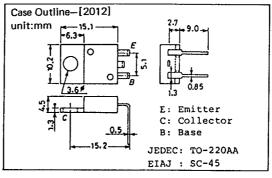


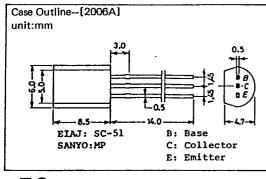


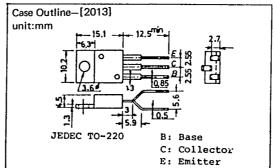


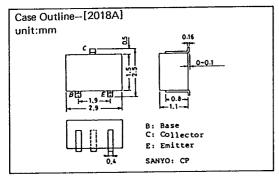


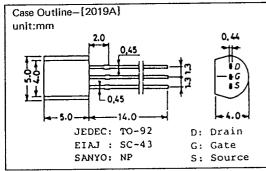


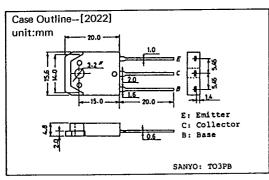


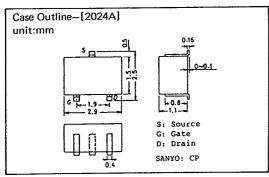


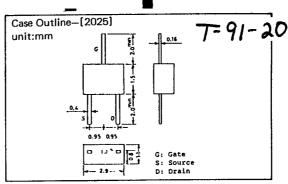


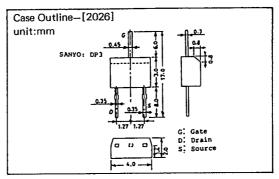


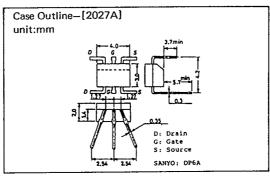


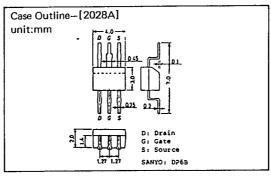


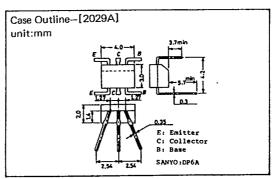


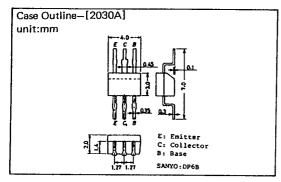


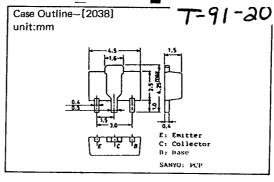


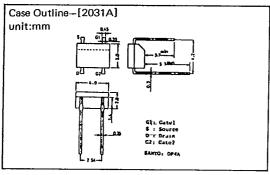


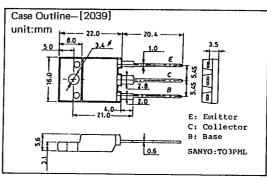


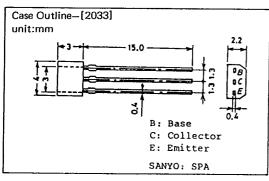


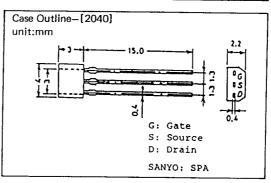


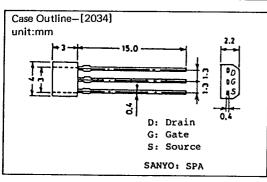


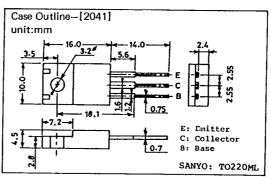


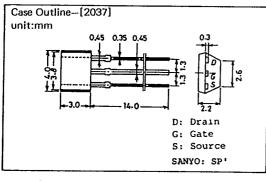


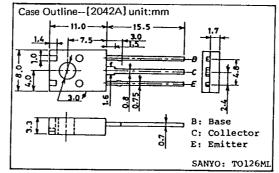


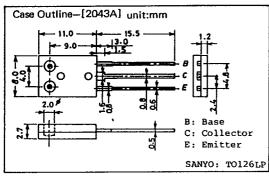


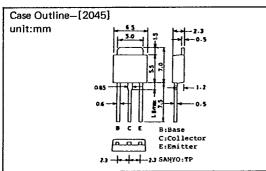


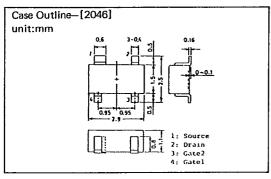


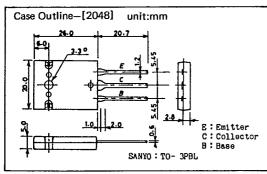


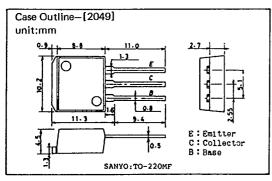


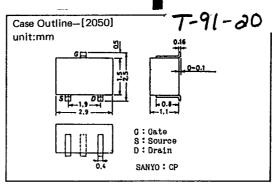


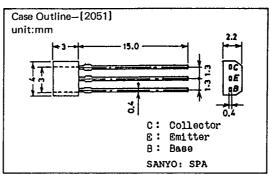


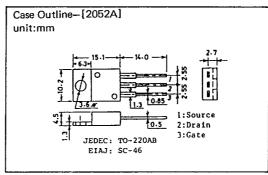


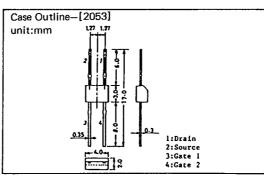


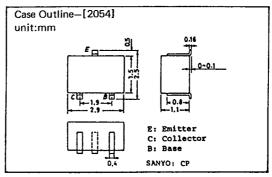


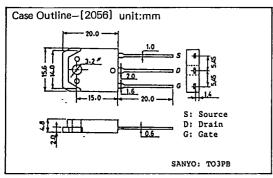


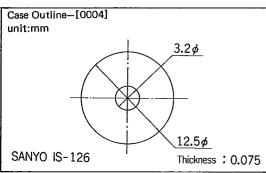


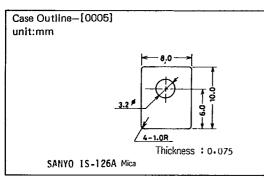


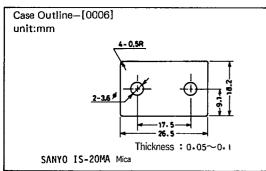


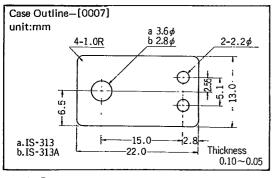












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