

PowerMOS transistor

BUK445-400A
BUK445-400B

T-39-09

GENERAL DESCRIPTION

N-channel enhancement mode field-effect power transistor in a plastic full-pack envelope.

The device is intended for use in Switched Mode Power Supplies (SMPS), motor control, welding, DC/DC and AC/DC converters, and in general purpose switching applications.

QUICK REFERENCE DATA

SYMBOL	PARAMETER	MAX.	MAX.	UNIT
	BUK445	-400A	-400B	
V_{DS}	Drain-source voltage	400	400	V
I_D	Drain current (DC)	4.0	3.8	A
P_{tot}	Total power dissipation	30	30	W
$R_{DS(ON)}$	Drain-source on-state resistance	0.8	1.0	Ω

MECHANICAL DATA

Dimensions in mm

Net Mass: 2g

Pinning:

1 = Gate

2 = Drain

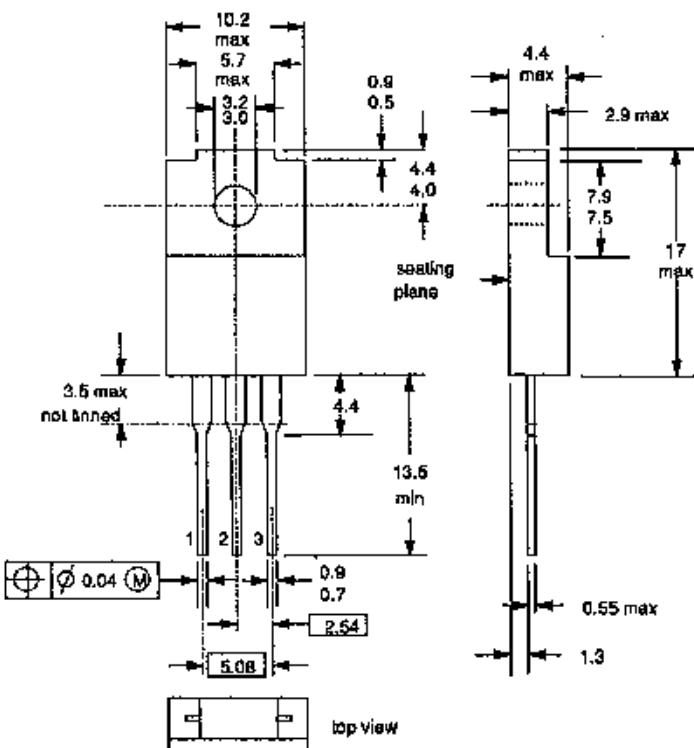
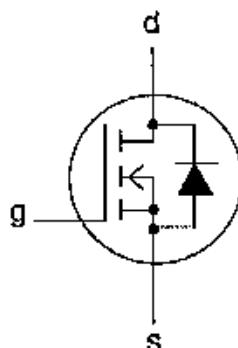


Fig. 1 SOT-186: The seating plane is electrically isolated from all terminals.

Notes

- Notes**

 1. Observe the general handling precautions for electrostatic-discharge sensitive devices (ESDs) to prevent damage to MOS gate oxide.
 2. Accessories supplied on request: refer to Mounting instructions for F-pack envelopes.

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RATINGS

Limiting values in accordance with the Absolute Maximum System (IEC 134)

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.		UNIT
V_{DS} V_{DGR} $\pm V_{GS}$	Drain-source voltage	-	-	400		V
	Drain-gate voltage	$R_{GS} = 20 \text{ k}\Omega$	-	400		V
	Gate-source voltage	-	-	30		V
I_D I_D I_{DM}	Drain current (DC)	$T_{JH} = 25^\circ\text{C}$	-	-400A	-400B	A
	Drain current (DC)	$T_{JH} = 100^\circ\text{C}$	-	4.0	3.6	A
	Drain current (pulse peak value)	$T_{JH} = 25^\circ\text{C}$	-	2.5	2.3	A
	Total power dissipation	$T_{JH} = 25^\circ\text{C}$	-	16	14	A
P_{tot} T_{sig} T_J	Storage temperature	-	-55	30		W
	Junction Temperature	-	-	150		'C
	-	-	-	150		'C

THERMAL RESISTANCES

From junction to heatsink	with heatsink compound	$R_{th(j-h)} = 4.1 \text{ K/W}$
From junction to ambient		$R_{th(j-a)} = 55 \text{ K/W}$

STATIC CHARACTERISTICS

 $T_{JH} = 25^\circ\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$V_{(BR)DSS}$	Drain-source breakdown voltage	$V_{GS} = 0 \text{ V}; I_D = 0.25 \text{ mA}$	400	-	-	V
$V_{os(0)}$	Gate threshold voltage	$V_{DS} = V_{GS}; I_D = 1 \text{ mA}$	2.1	3.0	4.0	V
I_{oss}	Zero gate voltage drain current	$V_{DS} = 400 \text{ V}; V_{GS} = 0 \text{ V}; T_J = 25^\circ\text{C}$	-	2	20	μA
I_{oss}	Zero gate voltage drain current	$V_{DS} = 400 \text{ V}; V_{GS} = 0 \text{ V}; T_J = 125^\circ\text{C}$	-	0.1	1.0	mA
I_{oss}	Gate source leakage current	$V_{GS} = \pm 30 \text{ V}; V_{DS} = 0 \text{ V}$	-	10	100	nA
$R_{on(on)}$	Drain-source on-state resistance	$V_{GS} = 10 \text{ V}; I_D = 2.5 \text{ A}$ $BUK445-400A$	-	0.7	0.8	Ω
$R_{on(on)}$	Drain-source on-state resistance	$I_D = 2.5 \text{ A}$ $BUK445-400B$	-	0.9	1.0	Ω

DYNAMIC CHARACTERISTICS

 $T_{JH} = 25^\circ\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
g_D	Forward transconductance	$V_{DS} = 25 \text{ V}; I_D = 2.5 \text{ A}$	3.5	4.5	-	S
C_{iss} C_{oss} C_{res}	Input capacitance Output capacitance Feedback capacitance	$V_{GS} = 0 \text{ V}; V_{DS} = 25 \text{ V}; f = 1 \text{ MHz}$	-	750 120 50	1000 180 70	pF
$t_{d(on)}$ t_r	Turn-on delay time Turn-on rise time	$V_{DD} = 30 \text{ V}; I_D = 2.7 \text{ A};$ $V_{GS} = 10 \text{ V}; R_{GS} = 50 \Omega;$	-	10 25	25 40	ns ns
$t_{d(off)}$ t_f	Turn-off delay time Turn-off fall time	$R_{gen} = 50 \Omega$	-	120 40	140 65	ns ns
L_d L_s	Internal drain inductance Internal source inductance	Measured from drain lead 6 mm from package to centre of die Measured from source lead 6 mm from package to source bond pad	-	4.5 7.5	-	nH

ISOLATION

 $T_{JH} = 25^\circ\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V_{isol}	Repetitive peak voltage from all three terminals to external heatsink	R.H. $\leq 65\%$; clean and dustfree	-	-	1500	V
C_{isol}	Capacitance from T2 to external heatsink	$f = 1 \text{ MHz}$	-	12	-	pF